

Docket No.: INTEL-0021



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

**EXPEDITED PROCEDURE
UNDER 37 C.F.R. § 1.116**

Edward A. BURTON, Kumar ANSHUMALI

Group Art Unit: 2818

Serial No.: 10/607,550

Filed: June 27, 2003

Examiner: Long K. Tran

Confirmation No.: 7414

Customer No.: 49623

For ADDITION OF METAL LAYERS WITH SIGNAL REALLOCATION TO A MICROPROCESSOR FOR
INCREASED FREQUENCY AND LOWER POWER

U.S. Patent and Trademark Office
Customer Window, Mail Stop AF
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Dear Sir:

Transmitted herewith is an Amendment and/or Reply in the above identified application.

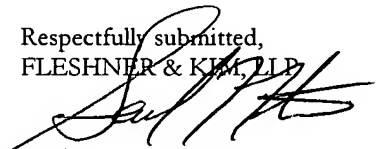
- ☒ No additional fee is required.
☐ Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	19	33	0	x \$50.00 =	
Independent Claims	1	3	0	x \$200.00=	
If multiple claims newly presented, add \$360.00					
Fee for extension of time					
TOTAL FEE DUE					-0-

- ☐ Please charge my Deposit Account No. 16-0607 in the amount of \$. An additional copy of this transmittal sheet is submitted herewith.
- ☐ A check in the amount of \$ _____ (Check # _____) is attached.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 16-0607, including any filing fees under 37 C.F.R. 1.16 for presentation of extra claims and any patent application processing fees under 37 C.F.R. 1.17.

Respectfully submitted,
FLESHNER & KIM, LLP


Mark L. Fleshner, Esq.
Registration No. 34,596
Samuel W. Ntiros, Esq.
Registration No. 39,318

P.O. Box 221200
Chantilly, VA 20153-1200
(703) 766-3701 MLF/SWN/lm
Date: **MARCH 16, 2006**
Q:\Documents\2112-021\89182

Intel Docket No: P15914 (2112-021)



**Expedited Procedures
Requested**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of : Assignee: Intel Corporation

BURTON, E. et al. : Confirmation No. 7414

Serial No. 10/607,550 : Examiner: Tran, L.

Filed: June 27, 2003 : Group Art Unit: 2818

For: ADDITION OF METAL LAYERS WITH SIGNAL REALLOCATION TO A
MICROPROCESSOR FOR INCREASED FREQUENCY AND LOWER POWER

AMENDMENT UNDER 37 CFR § 1.116

U.S. Patent and Trademark Office
Customer Service Window, Mail Stop AF
Randolph Building
401 Dulany Street
Alexandria, Virginia 22314

March 16, 2006

Sir:

The following amendments and remarks are submitted in response to the Final Office Action mailed on February 6, 2006, in connection with the above-identified application.

Amendments to the Claims are in the Listing of Claims beginning on page 2.

Remarks begin on page 6.